

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Hussein I. Hanafi, et al.

Examiner: Unassigned

Serial No: To be assigned

Art Unit: Unassigned

Filed: Herewith

Docket: YOR2002018US2 (15790A)

For: LOW RESISTANCE T-GATE MOSFET
DEVICE USING A DAMASCENE GATE
PROCESS AND AN INNOVATIVE
OXIDE REMOVAL ETCH

Dated: September 11, 2003

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, applicants submit the following references which applicants believe may be material to the above-identified patent application. A copy of the references which applicants wish to make of record in this case is enclosed herein for the Examiner's convenience along with a listing on Form PTO-1449 attached.

1. U.S. Patent No. 6,143,635, issued November 7, 2000 to Boyd, et al.;

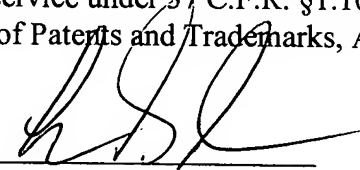
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Date of Deposit: September 11, 2003

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Alexandria, VA 22313-1450.

Dated: September 11, 2003



Leslie S. Szivos, Ph.D.

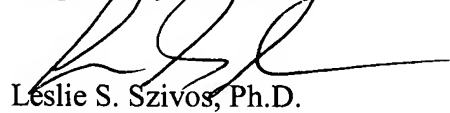
2. U.S. Patent No. 6,063,677, issued May 16, 2000 to Rodder, et al.;
3. U.S. Patent No. 6,040,214, issued March 21, 2000 to Boyd, et al.;
4. U.S. Patent No. 5,766,971, issued June 16, 1998 to Ahlgren, et al.;
5. U.S. Patent No. 5,282,925, issued February 1, 1994 to Jeng, et al.;
6. U.S. Patent No. 5,162,884, issued November 1992 to Liou et al.;
7. U.S. Patent No. 6,436,746, issued August 2002 to Chatterjee et al.; and
8. U.S. Patent No. 6,500,743, issued December 2002 to Lopatin et al..

Pursuant to 37 C.F.R. § 1.98 (d), copies of the references listed on the enclosed Form PTO-1449 are not provided since they were previously made record of in the parent application Serial No. 10/291,353 filed on November 8, 2002. References 1-5 were cited in the Information Disclosure Statement filed on November 8, 2002 and references 6-8 were cited by the Patent and Trademark Office in the Notice of Allowance dated July 16, 2002.

Consideration of this Information Disclosure Statement is respectfully requested, since the information provided herewith may be material to the examination of the present application as defined under 37 C.F.R. § 1.56. This statement is not intended to represent that a search has been performed or that no other art than that identified herein exists.

The instant Information Disclosure Statement is being submitted concurrent with the filing of the present application. Therefore, this filing is made under 37 C.F.R. § 1.97(b)(1). An Information Disclosure Statement filed under 37 C.F.R. § 1.97(b)(1) requires neither certification nor fee.

Respectfully submitted,



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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional) YOR920020182US2(15790A)		Application Number Unassigned
Applicant(s) Hussein I. Hanafi, et al.		
Filing Date Herewith	Group Art Unit Unassigned	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,143,635	11/07/00	Boyd, et al.			
		6,063,677	05/16/00	Rodder, et al.			
		6,040,214	03/21/00	Boyd, et al.			
		5,766,971	06/16/98	Ahlgren, et al.			
		5,282,925	02/01/94	Jeng, et al.			
		5,162,884	11/1992	Liou et al.			
		6,436,746	8/2002	Chatterjee et al.			
		6,500,743	12/2002	Lopatin et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.